THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1967 BY DIGITAL EQUIPMENT CORPORATION -O B -15 V ≶R2 ≶R3 ≶R6 ≶R9 ≨RII **₹R12** \$R13 ≶RI9 **▼**DII **▼**DI3 **♥**D21 **▼**D28 **₹**D23 **D38 ■**DIO **∡**DI **▲** D3 **D** 5 **A**D7 **DI2 ▲**D14 A 020 **A**D22 **▲**D24 **₹**029 **A** 026 **本**D31 **▲** D33 **₹ 85 1,500** \$R7 \$47 ≷RIO ≶1,500 R15 \$1,500 \$R17 \$47 R20 \$1,500 D18 D662 D37 D662 - DI7 D36 D662 D17 D16 D662 D35 D662 D27 D662 **★** D9 D662 六の 六C2 六04 D15 0662 D34 D662 -OC'A END UNLESS OTHERWISE INDICATED: RESISTORS ARE 1/4 W, 5% RESISTORS ARE 7,500 CAPACITORS ARE OI MFD DIODES ARE D664 USE THE ETCH BOARD OF THE G796 DATE 7-24-67 TRANSISTOR & DIODE CONVERSION CHART 2 mA LEVEL TERMINATOR G704 DRN. 7m. Waller CHK'D. 7-2467 TR

CHK'D. DATE
7/25/17 DEC

7/25/17 D662

ENG. OATE

PROD. DATE TERMINATO

EQUIPMENT SIZE COOF NUMBER
CORPORATION C CS G704-0-1 DEC EIA DEC IN645 IN3606 AYNARD, MASSACHUSETTS PRINTED CIRCUIT REV. A B